CofC

PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States • Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450,

Alexandria, VA 22313-1450.

November 28,2005

Carolyn L. Ross

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Tim Murphy and Lee Gotcher

Attorney Docket No.: 501039.03

Patent No. : US 6,936,489 B2

Serial No. : 10/651,594

Issue Date: August 30, 2005

Filed

: August 29, 2003

Title : ME

: METHOD AND SYSTEM FOR ELECTRICALLY COUPLING A CHIP TO CHIP

PACKAGE

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate
DEC 0 8 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Date

A Certificate of Correction under 35 U.S.C. § 254 is respectfully requested for the above-identified patent in order to correct Patent and Trademark Office errors made during the printing of the patent. The changes in the patent needed to correct the errors are as follows:

Column, Line Reads Should Read

Column 3, Line 67 "of the chip 402, the infrared --of the chip 402, the infrared

signals 406 propagate though" signals 406 propagate through--

The above errors for which correction is requested under 35 U.S.C. § 254 were made in the printing of the patent. The errors are considered sufficiently important to justify the processing of a Certificate of Correction under 35 U.S.C. § 254. A Form PTO-1050, in duplicate, is enclosed herewith.

Favorable consideration of this Request is respectfully requested.

Respectfully submitted,

Date: 1/2005

Bv:

Edward W. Bulchis, Reg. 26,847

Customer No. 27,076
Dorsey & Whitney LLP
1420 Fifth Avenue, Suite 3400
Seattle, WA 98101
(206) 903-8785
Attorney for Applicant(s)

EWB:clr

Enclosures:

Postcard

Form PTO-1050 (+ copy)

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Applicants: Tim Murphy and Lee Gotcher

Attorney Docket No.: 501039.03

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.

US 6,936,489 B2

DATED

August 30, 2005

INVENTOR(S)

Tim Murphy and Lee Gotcher

It is certified that error appears in the above identified patent and that said Letters Patent is hereby corrected as shown below:

Column, Line	Reads	Should Read		
Column 3, Line 67	"of the chip 402, the infrared signals 406 propagate though"	of the chip 402, the infrared signals 406 propagate through		
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Patent No. <u>US 6,936,489 B2</u>

DORSEY & WHITNEY LLP 1420 Fifth Avenue, Suite 3400 Seattle, Washington 98101

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FORM PTO-1050 (REV. 3-82)

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US 6,936,489 B2

DATED

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INVENTOR(S)

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Column, Line	Reads	Should Read
Column 3, Line 67	"of the chip 402, the infrared signals 406 propagate though"	of the chip 402, the infrared signals 406 propagate through
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